Heterointerface Engineering of Broken-Gap InAs/GaSb Multilayer Structures

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ABSTRACT: Broken-gap InAs/GaSb strain balanced multilayer structures were grown by molecular beam epitaxy (MBE), and their structural, morphological, and band alignment properties were analyzed. Precise shutter sequence during the MBE growth process, enable to achieve the strain balanced structure. Cross-sectional transmission electron microscopy exhibited sharp heterointerfaces, and the lattice line extended from the top GaSb layer to the bottom InAs layer. X-ray analysis further confirmed a strain balanced InAs/GaSb multilayer structure. A smooth surface morphology with surface roughness of ~0.5 nm was demonstrated. The effective barrier height ~0.15 eV at the GaSb/InAs heterointerface was determined by X-ray photoelectron spectroscopy, and it was further corroborated by simulation. These results are important to demonstrate desirable characteristics of mixed As/Sb material systems for high-performance and low-power tunnel field-effect transistor applications.

KEYWORDS: broken gap, tunnel transistors, InAs/GaSb, molecular beam epitaxy, heterointerface

INTRODUCTION

Scaling down metal-oxide-semiconductor field-effect transistors below 10 nm and high power consumption at OFF-state are important issues for a nanoscale silicon transistor. Low supply voltage operation and steep switching characteristics are essential for a next-generation low-power nanoscale transistor. Tunnel field-effect transistors (TFETs) with sub-60 mV/dec subthreshold swing (SS) based on the band-to-band tunneling mechanism at room temperature can help to realize low OFF-state leakage at low supply voltage of operation. Very recently, mixed arsenide/antimonide based In$_x$As/Ga$_{1-x}$Sb TFET device structures with variable arsenic (As) and antimony (Sb) alloy compositions for a wide range of adjustable effective tunnel barrier height ($E_{\text{eff}}$), such that these two layers are internally lattice matched, were extensively studied by several researchers.\textsuperscript{1-3} In such configuration, controlling alloy compositions while simultaneously maintaining the internally lattice matched condition is extremely challenging due to the formation of an unwanted layer by surface termination of atoms at the source/channel heterointerface. However, mixed As/Sb based heterostructures provide attractive band gaps and tunable band alignment for a p-type TFET with In$_{1-x}$Ga$_x$As/Ga$_{1-x}$Sb as a source/channel configuration. Furthermore, a p-type InAs/GaSb TFET exhibited an ON current of $I_{\text{ON}} = 85 \mu A/\mu m\textsuperscript{2}$ and the same structure in n-channel configuration revealed an ON current of $I_{\text{ON}} = 180 \mu A/\mu m$ at $V_{GS} = V_{DS} = 0.5$ V with $I_{\text{ON}}/I_{\text{OFF}}$ ratio of $6 \times 10^7$. In this InAs/GaSb TFET configuration, the ideal band alignment would be a broken gap configuration. However, the local strain at the InAs/GaSb interface depends on two types of interfacial bonds: tensile Ga–As bonds and compressive In–Sb bonds.\textsuperscript{4} As a result of these interfacial bonds, the interface could be GaAs-like due to Ga/In exchange during the growth of an InAs/GaSb TFET structure. Moreover, different types of interfacial layers have distinct strain relaxation properties and one (GaAs-like) will introduce large number of dislocations at the 0.62% tensile strained InAs on GaSb substrate that will increase the OFF-state current in an InAs/GaSb TFET.\textsuperscript{5} On the other hand, the formation of InSb-like interfacial layer at the InAs/GaSb heterointerface would compensate the tensile strain, and an additional compressive strain amount will help to decrease the defect density and prevent strain relaxation. As a result, one could achieve strain balanced InAs/GaSb multilayer heterostructures by controlling proper thicknesses of each layer and switching sequences of As, Sb, In, and Ga at each heterointerface during growth. However, measures must be taken during the molecular beam epitaxy (MBE) growth of mixed As/Sb based heterostructures due to a strong competition between different incorporation rates of anions (As and Sb) due to their dissimilar sticking coefficients and the metallic character of Sb.

This study focuses on the growth, structural, and morphological properties, strain relaxation, band alignment, and simulated $E_{\text{eff}}$ of the strain balanced InAs/GaSb multilayer...
structure grown by solid source MBE. The strain compensation was achieved by carefully controlling the shutter sequences of growth species at each heterointerface and proper thicknesses of each layer during growth. The strain relaxation properties of InAs/GaSb heterostructures was evaluated using high-resolution X-ray diffraction. Cross-sectional transmission electron microscopy (TEM) was used to investigate the defect properties and surface morphology was analyzed using atomic force microscopy (AFM). The X-ray photoelectron spectroscopy (XPS) measurement was carried out to determine the bond alignment at the InAs/GaSb heterointerface and the experimental band offset values were further confirmed by modeling.

■ RESULTS AND DISCUSSION

Material Characterization. Figure 1a shows the schematic of the InAs/GaSb multilayer heterostructure selected for this work, where the thickness of each layer is indicated in this figure, and Figure 1b shows the shutter sequences for this growth. The nominal thickness of each layer is about 15 nm, and the lattice mismatch between the InAs and GaSb is about 0.62%. To achieve well-controlled coherent heterointerface, GaAs-like interfacial layer must be prevented during growth. In the shutter sequence, two methods were used to form InSb-like interfacial layer: (1) InSb stacking and (2) Sb soaking. After the completion of GaSb layer growth, the Sb flux was left open for 6 s followed by 4 s of indium (In) exposure to create an artificial thin InSb layer in between the GaSb and InAs layer. The As flux was open for the duration of the InAs layer growth, and prior to the GaSb deposition, the InAs surface was soaked by Sb for 6 s. Because the vapor pressure of As is higher than that of Sb, the exposure of Sb on the surface of InAs for a short duration helps prevent As from escaping the surface of InAs prior to GaSb layer growth. Thus, through the subtle control of the InAs/GaSb epitaxial heterointerface, we could achieve the overall strain balanced structure, and both the InAs and GaSb layers are internally lattice matched in the growth sequence studied here. The InAs/GaSb substrate interface is tensile (T) and GaSb/InAs interface is compressive (C) as indicated in Figure 1a, and the equal number of C/T layers balances the strain, which can be seen from the cross-sectional TEM as well as X-ray analysis below.

The InAs/GaSb multilayer heterostructure was investigated by cross-sectional TEM measurement and analysis. Figure 2a shows the high-resolution TEM micrographs of the InAs/GaSb multilayer stack consisting of 4 layer (2 tensile interface and 2 compressive interface). In this figure, each layer and interface were labeled to match with the growth sequence of the multilayer stack, as shown in Figure 1a. Figure 2c,d shows high-resolution lattice indexing of the epitaxial InAs layer on GaSb buffer layer and epitaxial GaSb on InAs epilayer, respectively. These TEM micrographs showed that the epilayers were homogeneous and with smooth heterointerfaces. In the InAs-on-GaSb interface, an intentionally added 4 s of growth time enabled the InSb interfacial layer to achieve a high-quality heterointerface, and this layer will compensate the tensile strain amount of 0.62% between InAs and GaSb. The mixture fluxes of both In and Sb limited the As coverage, which tends to form GaAsSb, thus accumulating more tensile strain at the InAs-on-GaSb heterointerface. Furthermore, Sb soaking after the InAs deposition also restricted As coverage and prevented the formation of a GaAs-like layer. In fact, the exposure of Sb on the surface of InAs prior to GaSb growth prevents the As from escaping the InAs surface and acts as a surfactant during growth. Both methods tailored to create InSb-like interfacial layer at each heterointerface. The InSb-like interfaces provide strain compensation and virtually defect-free heterointerfaces. Each interface exhibited an abrupt heterojunction, which is essential for high-performance TFET structure that can reduce the leakage current under reverse gate voltage.

High-resolution X-ray diffraction was used to investigate the strain relaxation properties of InAs/GaSb multilayer layer structure. Figure 3 shows X-ray rocking curve of 18 nm GaSb/15 nm InAs/18 nm GaSb/15 nm InAs/GaSb substrate.
structure. A simulation was performed to identify the position of the InAs peak from thickness fringes. In Figure 3, a green line shows the simulated rocking curve from 15 nm InAs layer on GaSb substrate with the addition of an ultrathin 0.5 nm InSb interfacial layer. One can locate the InAs peak position with respect to the GaSb substrate peak. However, the simulated rocking curve with 0.5 nm InSb interfacial layer at InAs on GaSb and GaSb on InAs heterointerface exhibited that the InAs layer was affected in the multilayer configuration. Besides, InAs layers provide an envelope function for fringes next to the GaSb peak and have a peak-pulling effect due to the multilayer structures. The simulated rocking curve also shows the complexity of fringes due to InAs/GaSb multilayer structure with an InSb interfacial layer. The simulation further confirms the existence of interfacial layers, shown in Figure 2b–d. Figure 4 shows the symmetric (004) and asymmetric (115) reciprocal space maps (RSMs) of the layer structure, respectively. The envelope line of three secondary fringes were InAs epilayers affected by multilayer structures.

Figure 4. (a) Symmetric (004) and (b) asymmetric (115) reciprocal space maps of InAs/GaSb multilayer structure. The thickness fringes become contour of intensity. All the thickness fringes of InAs and GaSb RLPs lie along the fully strained vertical line.

Figure 5 shows the surface morphology from the surfaces of 2 \times 2 \mu m and 5 \times 5 \mu m area scans. The surface root-mean-square (rms) roughness were found to be \sim 0.42 and 0.55 nm, respectively. Smooth surface morphology implies minimal dislocations inside the epilayer structure, which is supported by both TEM and X-ray analysis above. Furthermore, lower surface roughness can also be related to well-controlled elements switching during growth. In fact, lower rms surface roughness was reported by creating an ultrathin layer of InAs-like interface during the growth of InGaAs/GaAs TFET structure.

**Band Offset.** The band offset parameters at the InAs/GaSb heterointerface are an important design parameter for a broken gap InAs/GaSb based tunnel FET structure. X-ray photoelectron spectroscopy spectra revealed binding energy information for each materials. The core levels (CLs) and valence band maxima (VBM) spectra were recorded to determine the band alignment between the GaSb and the InAs layer. The band alignment properties were determined by Kraut’s method. The valence band offset (VBO) between the GaSb and the InAs can be expressed as

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\Delta E_V = (E_{GaSb}^{1/2} - E_{InAs}^{1/2}) - (E_{InAs}^{VB} - E_{GaSb}^{VB}) = \Delta E_{CL}
\]

where \(E_{GaSb}^{1/2}\) and \(E_{InAs}^{1/2}\) are CL binding energy of Sb 4d\(5/2\) and As 3d\(5/2\) from GaSb and InAs layer, respectively; \(E_{InAs}^{VB}\) and \(E_{GaSb}^{VB}\) were VBMs for the respective materials. \(\Delta E_{CL} = E_{GaSb}^{1/2} - E_{InAs}^{1/2}\) is the difference in CL binding energy from the GaSb
and InAs heterointerface. Figure 6 shows the CL binding energies and corresponding VBMs from the thick InAs layer, thick GaSb layer, and the GaSb/InAs heterointerface. Using the measured data and the equation above, the VBO was determined to be 0.51 ± 0.05 eV. A constant flow of electrons was used throughout the XPS measurements to compensate the charging effect. Using the bandgap values of InAs ($E_{\text{GInAs}} = 0.36$ eV) and GaSb ($E_{\text{GGaSb}} = 0.72$ eV) at 300 K, the effective barrier height, $E_{\text{beff}} = E_{\text{GInAs}} - \Delta E_{\text{V}}$ of −0.15 eV was determined, as shown in Figure 6d. It is worth noting that the band bending effect caused by the semiconductor-to-semimetal transition should be considered during determining the band alignment especially in the broken gap system.8–10 To address this issue, we have measured XPS spectra from the bulk InAs and homoepitaxial 1 μm thick epi-GaSb on GaSb substrate. The energy difference between the CL and the VBM for a thick 20 nm epi-InAs and the bulk InAs substrate are 40.56 and 40.51 eV, respectively, as shown in Figure 6a. Similarly, the energy differences are 31.87 and 31.83 eV for 18 nm GaSb and 1 μm thick epi-GaSb on GaSb substrate, respectively, shown in Figure 6b. One can find from Figure 6a,b that the difference (CL to VBM) between the epi- and bulk InAs and GaSb layers are 0.05 and 0.04 eV for InAs and GaSb, respectively. These values are within the XPS measurement error bar.11 This $E_{\text{beff}}$ value is in agreement with the previously reported results.12–14 Here, the negative value indicates the broken gap configuration at the GaSb/InAs heterointerface. This $E_{\text{beff}}$ value determines the ON- and OFF-state current of a mixed As/Sb based tunnel FET device structure.

To correlate the experimental band alignment values and hence the $E_{\text{beff}}$ as a function of In and Sb alloy compositions in InGaAs/GaAsSb TFET structure, we performed the modeling using TCAD Sentaurus, version H. Figure 7a shows both simulated and experimental $E_{\text{beff}}$ corresponding to a various In and Sb compositions in $\text{In}_{x}\text{Ga}_{1-x}\text{As}/\text{GaAs}_{1-y}\text{Sb}_{y}$ system such that for each In and Sb composition, the InGaAs and GaAsSb layers are internally lattice matched. Simulation model was calibrated with the experimental band alignment value with the In composition of $x = 0.7$ and the Sb composition of $y = 0.65$.15 One can find from this Figure 7a that the $E_{\text{beff}}$ value decreases from 0.44 eV where $x = 0.53$ and $y = 0.49$ (staggered gap) to −0.1 eV where $x = 1$ and $y = 1$ (broken gap) with increasing In and Sb compositions. The lowest simulated staggered gap $E_{\text{beff}}$ is 0.06 eV for an In$_{0.9}$Ga$_{0.1}$As/GaAs$_{0.1}$Sb$_{0.9}$ TFET structure. The results of simulated $I_{\text{ON}}$ as a function of $E_{\text{beff}}$ are shown in Figure 7b. The inset shows the drain current, $I_{\text{D}}$, with gate voltage swing. To improve the ability of gate control in a nanoscale TFET, the double gate p-i-n TFET (channel thickness of 5 nm), long channel (channel length of 40 nm), and low equivalent oxide thickness (EOT of 1 nm) were used in this simulation. Asymmetric doping of source and drain values of $4 \times 10^{19}$ cm$^{-3}$ and $6 \times 10^{17}$ cm$^{-3}$, respectively, were considered to prevent ambipolar current conduction. It was found that the $I_{\text{ON}}$ current is strongly dependent on the $E_{\text{beff}}$ under $V_{\text{GS}} = V_{\text{DS}} = 0.5$ V conditions. The increase in $I_{\text{ON}}$ in this...
This calculation. This even higher ON current can be attainable, but measures must also be taken to reduce the OFF-state leakage of such structure. The quantization and strain effect were not taken into account in this calculation. This first-order E_{eff} calculation, which is in agreement with the experimental results,\textsuperscript{15–17} provides critical guidance for designing mixed As/Sb-based tailor-made tunnel FET device structure.

**CONCLUSIONS**

In summary, high-quality broken gap strain balanced InAs/GaSb multilayer structure, grown using solid source MBE, with InSb-like interfacial layer were demonstrated by analyzing structural, morphological, and band alignment properties. Cross-sectional TEM micrographs exhibited sharp heterointerfaces and lattice lines extended from the top GaSb layer to the bottom InAs layer. X-ray analysis further confirmed a strain balanced InAs/GaSb multilayer structure. A smooth surface morphology with surface roughness of about 0.5 nm was achieved. The effective barrier height at the GaSb/InAs heterointerface was determined to be \(-0.15\) eV by X-ray photoelectron spectroscopy, which is in agreement with other reported results. This was further corroborated by simulation. These results are important to demonstrate desirable characteristics of mixed As/Sb material systems for superlattice and tunnel FET applications. Thus, one can tailor the energy barrier of internally lattice matched GaSb/InGaAs heterointerface by precisely controlling the growth sequences of antimony- and arsenide-based material systems.

**MATERIALS AND METHODS**

**Material Synthesis.** The periodic InAs and GaSb layers were grown by solid source MBE on (100) GaSb substrate with 215 nm undoped GaSb buffer layer at 550 °C with As/In = 20, Sb/Ga = 5 and a growth rate of \(-0.35\) μm/h and \(-0.43\) μm/h, respectively. The GaSb substrate oxide desorption was performed under Sb flux at 660 °C. A low-temperature Sb effusion cell was used for the Sb flux during growth. The surface pattern was constantly monitored by reflection high-energy electron diffraction and during the entire layer structure growth. The thickness of each layer and the shutter sequences were carefully selected such that the InAs/GaSb material system is internally strain balanced. This was performed by creating an ultrathin layer of InSb(As) in between the InAs and the GaSb.

**Materials Characterization.** To determine the structural quality and the relaxation state of InAs/GaSb multilayer stack, we recorded high-resolution triple axis X-ray rocking curve. Cross-sectional high-resolution transmission electron microscopy (HR-TEM) was used to characterize the heterointerface between the InAs and the GaSb as well as GaSb and InAs epilayer. HR-TEM imaging was performed using JEOL 2100 transmission electron microscope. For this purpose, the electron transparent foils of thin film cross-section of InAs/GaSb were prepared by standard polishing technique (i.e., mechanical grinding, dimpling and Ar\(^+\) ion beam milling). The band alignment of InAs/GaSb heterointerface was investigated using a PHI Quantera SXM XPS system with a monochromated Al K\(_\alpha\) (energy of 1486.7 eV) X-ray source. The As 3d\(_{5/2}\) and Sb 4d\(_{5/2}\) CL binding energy spectra were collected with a pass energy of 26 eV and an exit angle of 45°.

The binding energy was corrected by adjusting the carbon (C) 1s CL peak line. The uncertainty of \(\Delta\)E value was found to be in the range of \(\pm0.05\) in the present work by the regressions analysis of selected data over the linear region.

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**Notes**

The authors declare no competing financial interest.

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Figure 7. (a) Experimental and simulated E_{eff} as a function of In and Sb compositions in GaAs\(_{1-x}\)Sb\(_{x}\)/In\(_{x}\)Ga\(_{1-x}\)As material system. The experimental results are in agreement with the simulated prediction. (b) Simulated I_{ON} as a function of effective tunnel barrier height.

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Material system is believed to be due to the increase in tunneling probability compared with the material system of 0.35 eV tunnel barrier height. However, compositions lower than In\(_{0.8}\)Ga\(_{0.2}\)As/Ga\(_{0.1}\)Sb\(_{0.9}\) system, the E_{eff} is negative, and even higher ON current can be attainable, but measures must be taken to reduce the OFF-state leakage of such structure. The quantization and strain effect were not taken into account in this calculation. This first-order E_{eff} calculation, which is in agreement with the experimental results,\textsuperscript{15–17} provides critical guidance for designing mixed As/Sb-based tailor-made tunnel FET device structure.

Figure 7. (a) Experimental and simulated E_{eff} as a function of In and Sb compositions in GaAs\(_{1-x}\)Sb\(_{x}\)/In\(_{x}\)Ga\(_{1-x}\)As material system. The experimental results are in agreement with the simulated prediction. (b) Simulated I_{ON} as a function of effective tunnel barrier height.
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